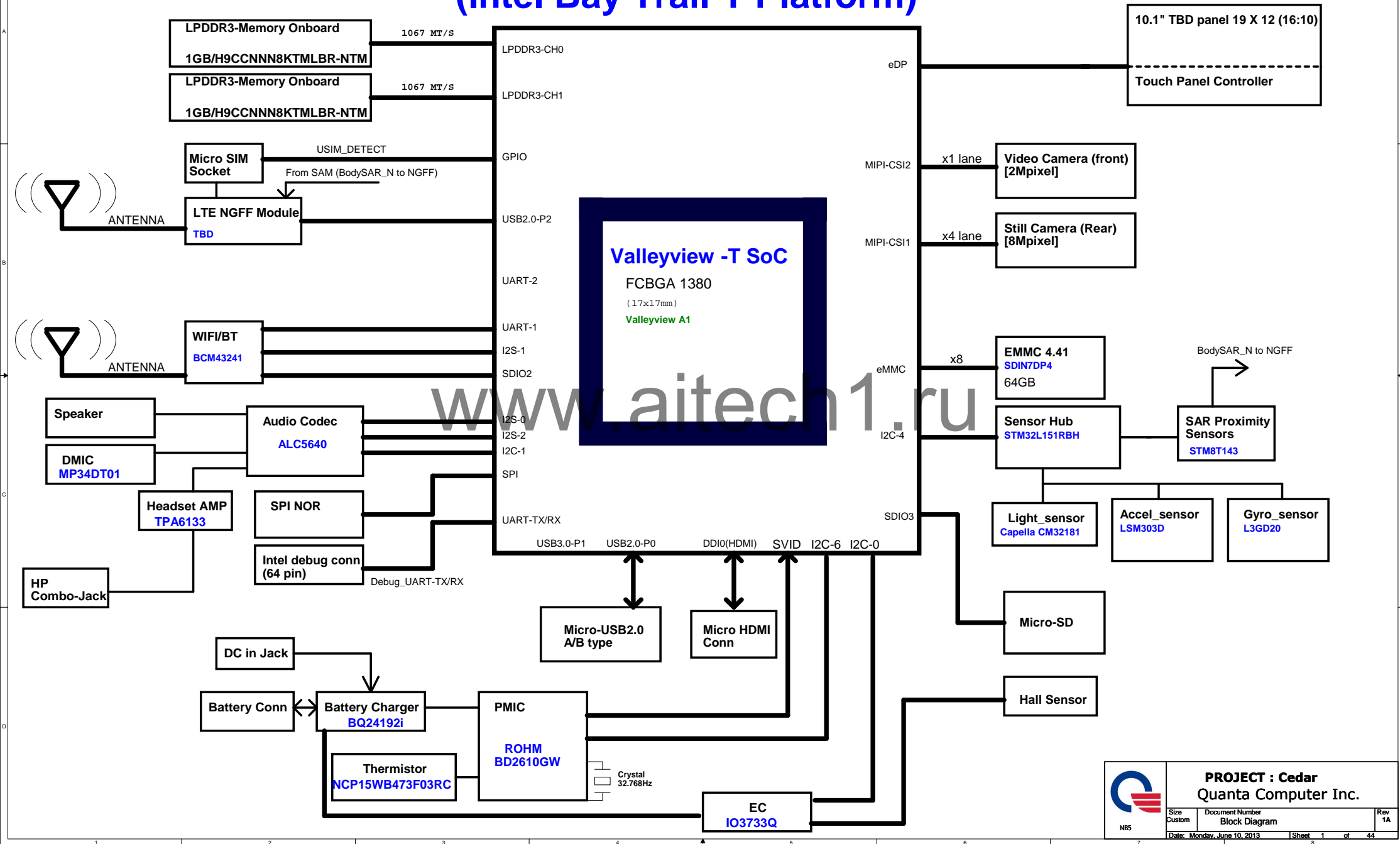
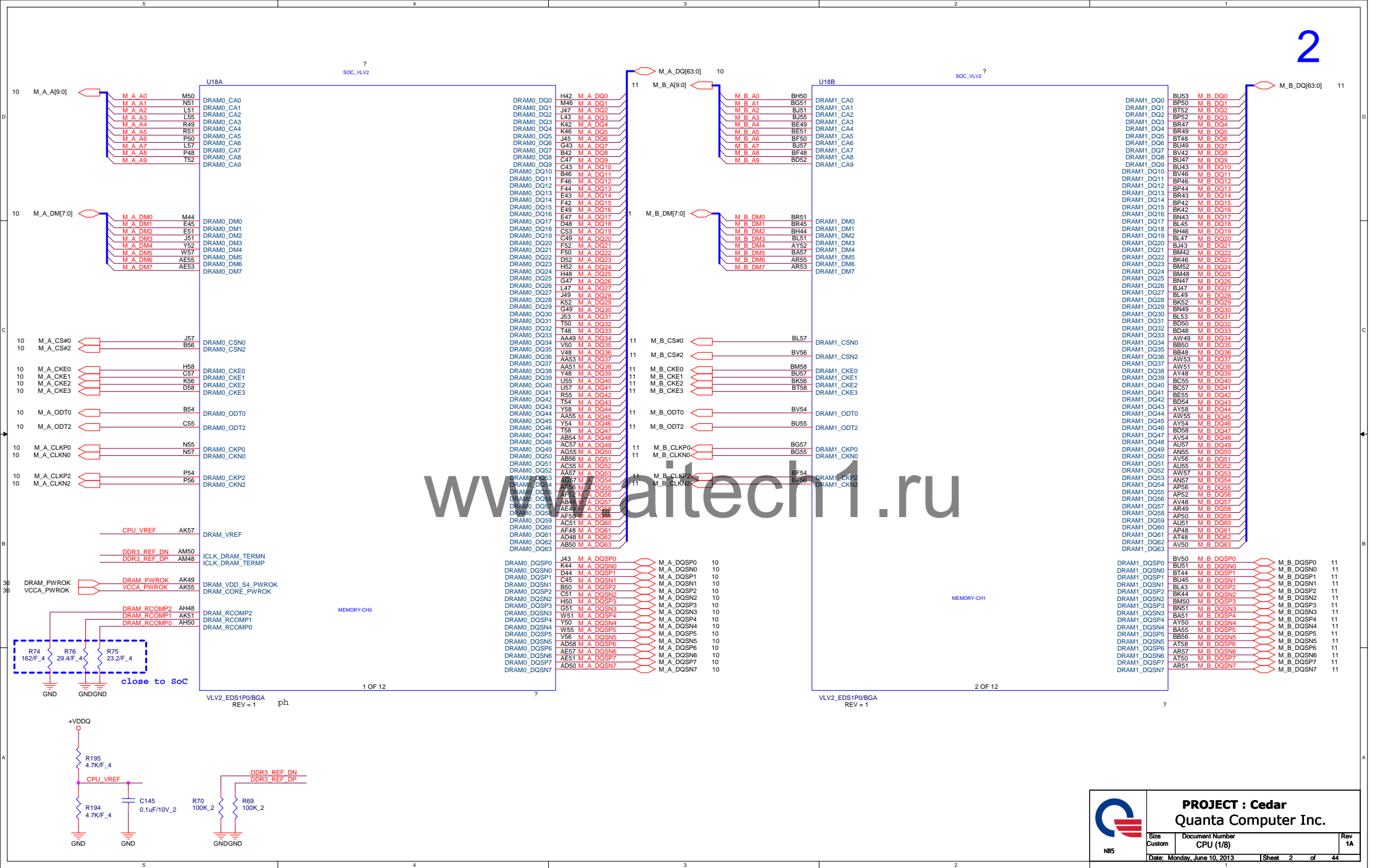
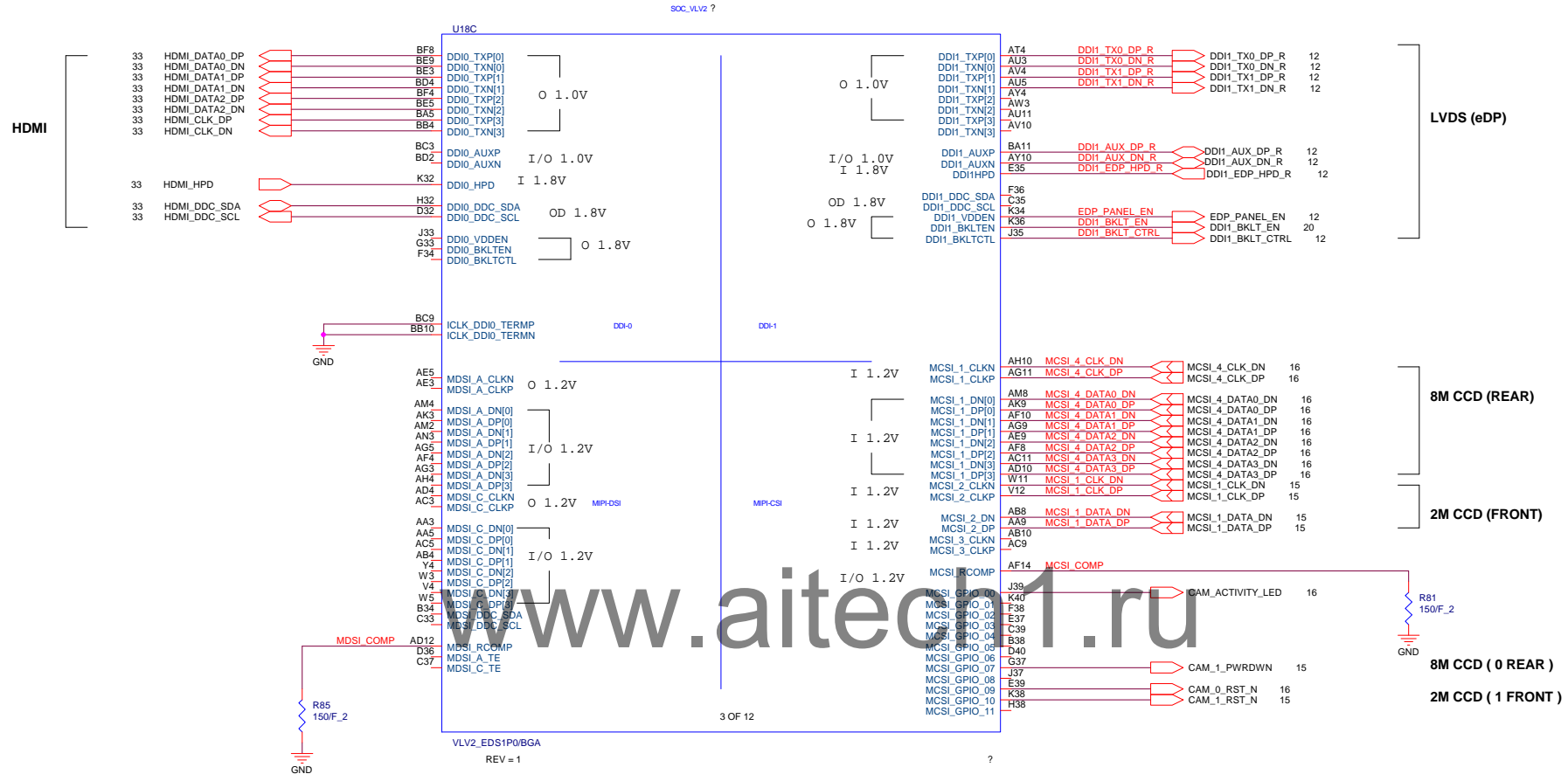


# Cedar Block Diagram (Intel Bay Trail-T Platform)

01

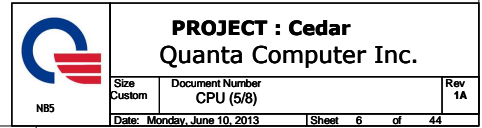


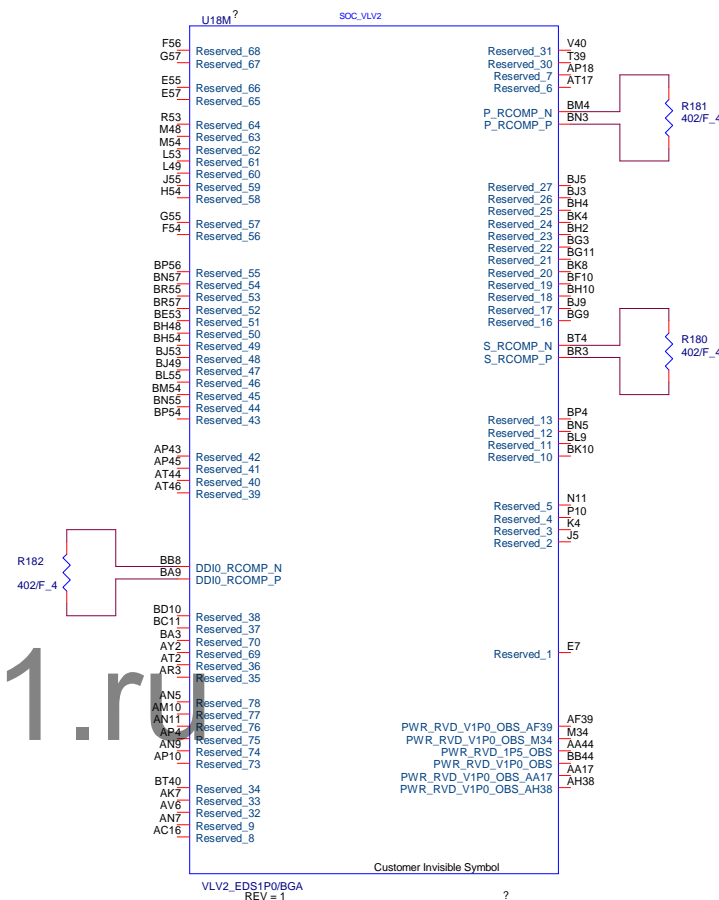
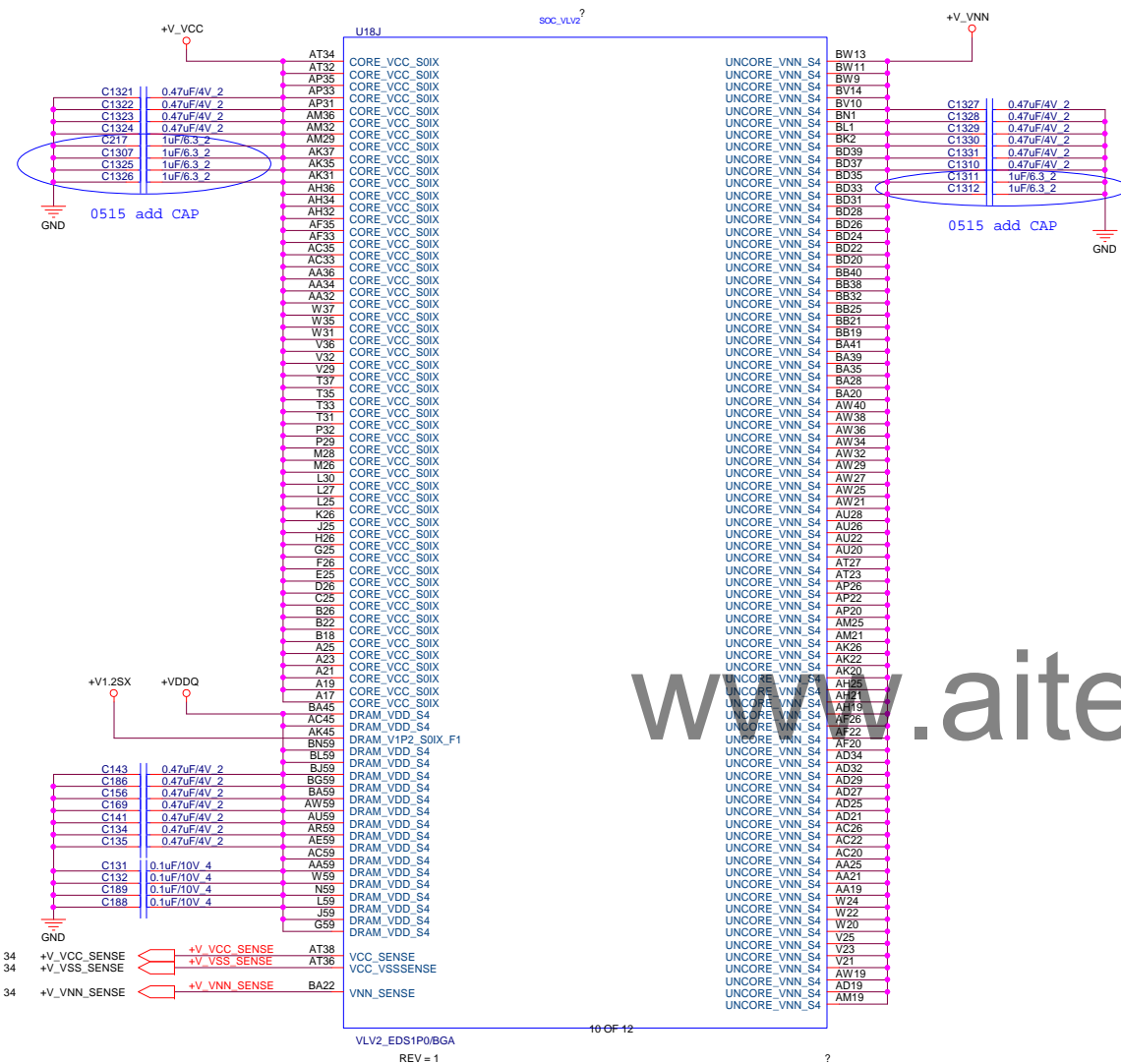


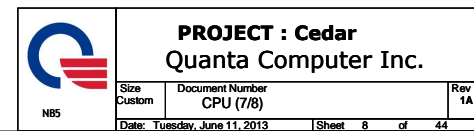
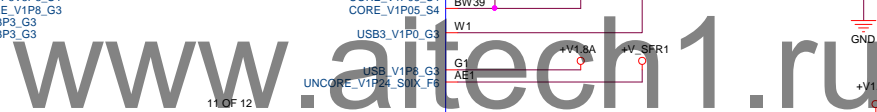




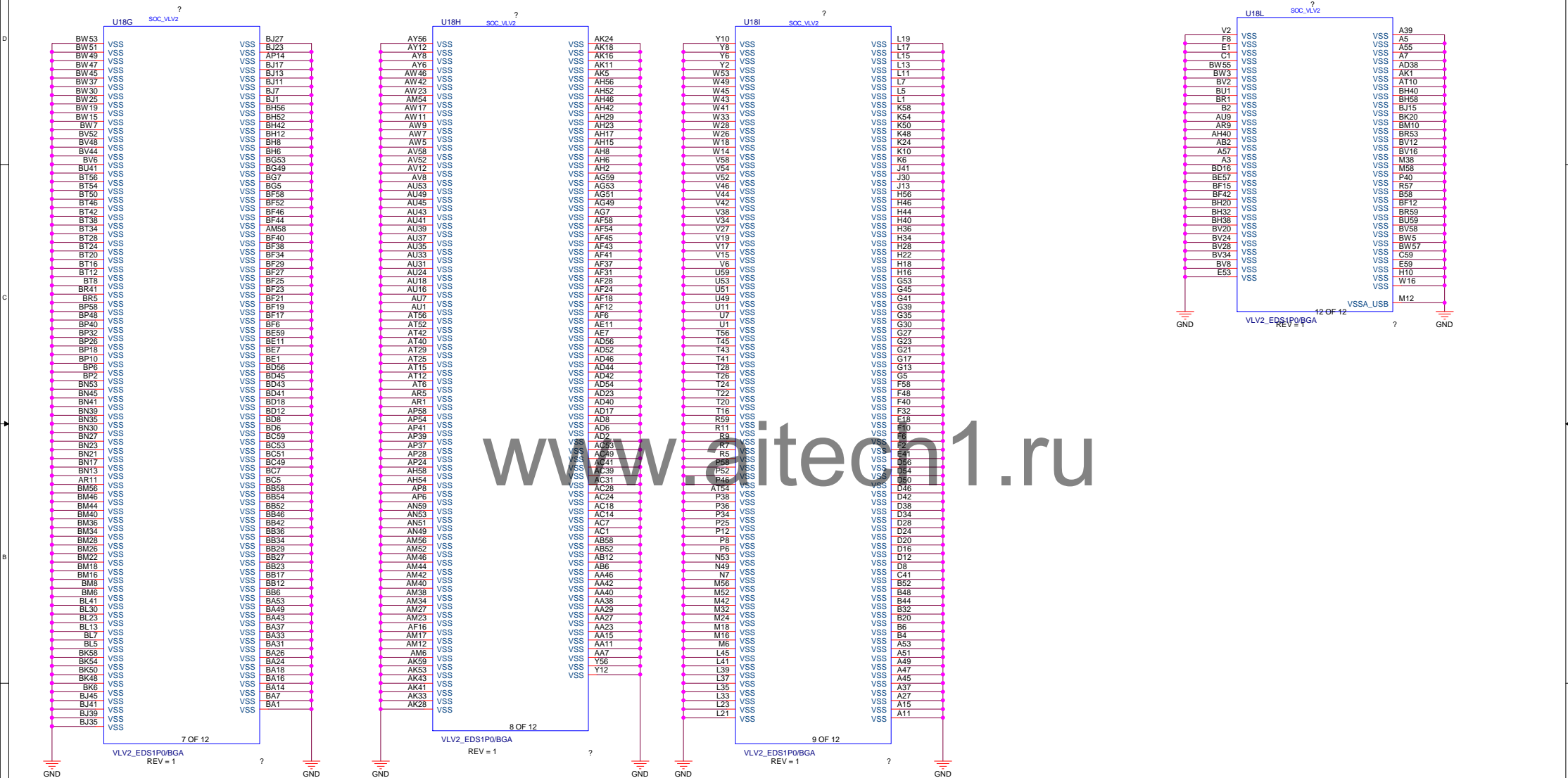




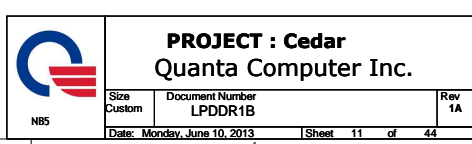












VLED


Close to LCD Connector

C89 0.1uF/25V 4

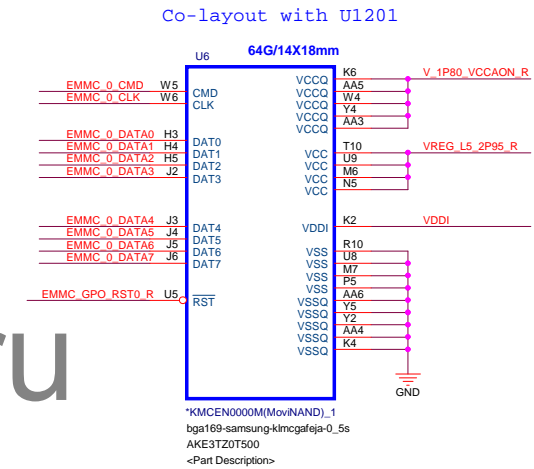
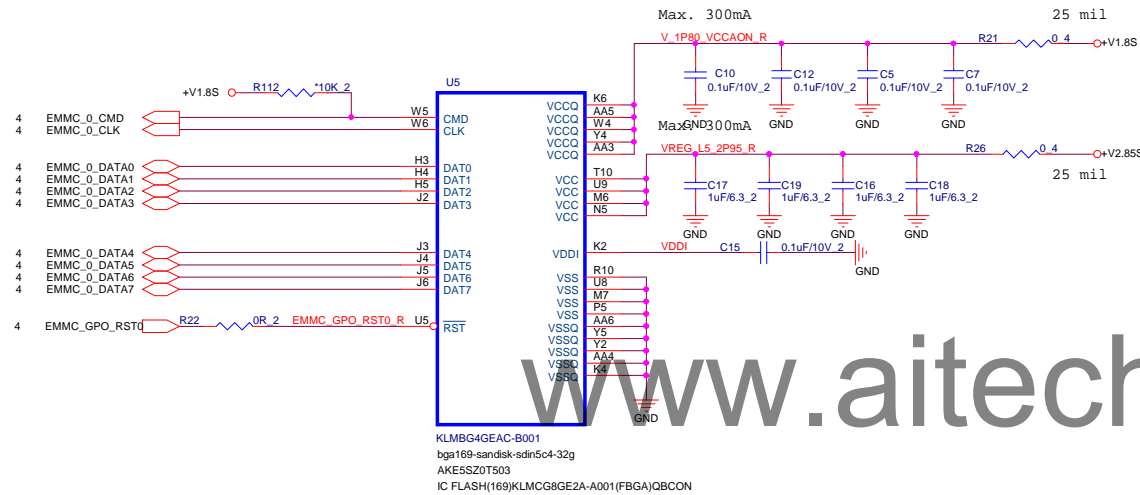
GND

3.4V

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 NB5	<b>PROJECT : Cedar</b>		
	<b>Quanta Computer Inc.</b>		
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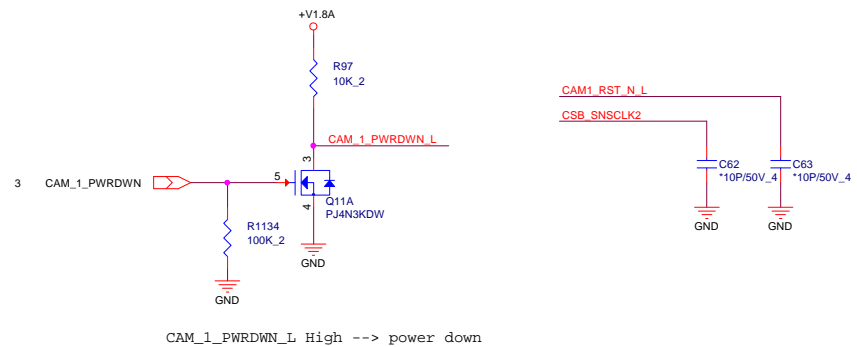
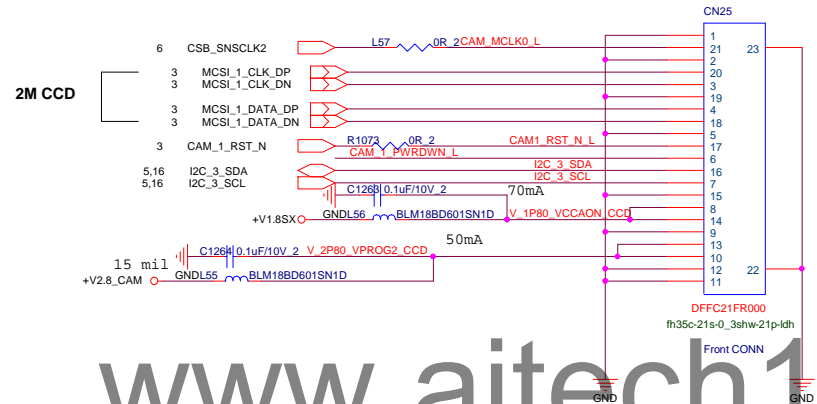
# EMMC



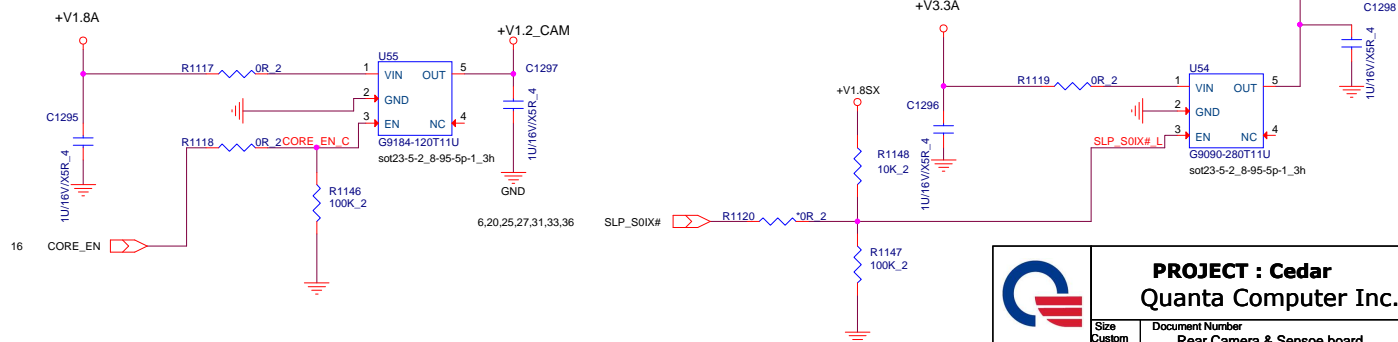
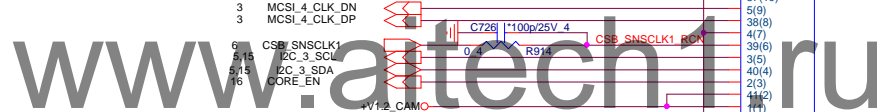
0527 add 2nd source

iNAND (eMMC)				
QBCON	TOPB/S	Vender PN	SIZE	
AKE3Z2PT508	AKE3Z2PT507	KLMCG8GE2A-A001	64G	Samsung
AKE5SZ0T500	AKE5SZ0T503	KLMBG4GEAC-B001	32G	Samsung
AKE3TZ-T101	AKE3TZ-T100	SD1N8DE4-64G	64G	Sandisk
AKE3SZ-T101	AKE3SZ-T100	SD1N8DE4-32G	32G	Sandisk

## Front Camera Module Connector

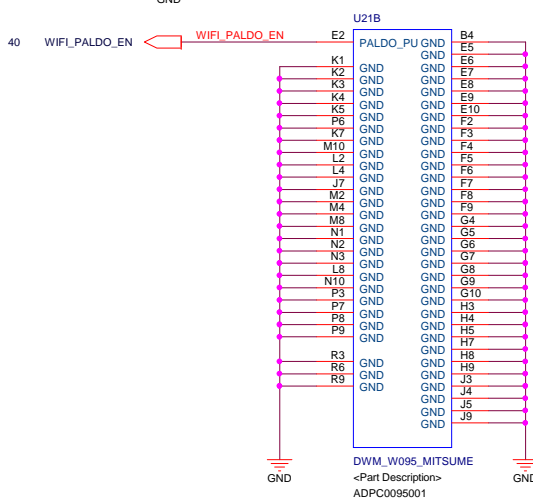
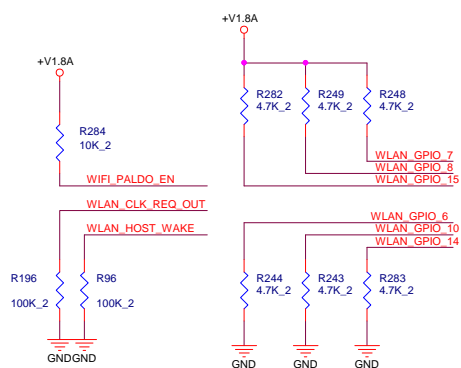
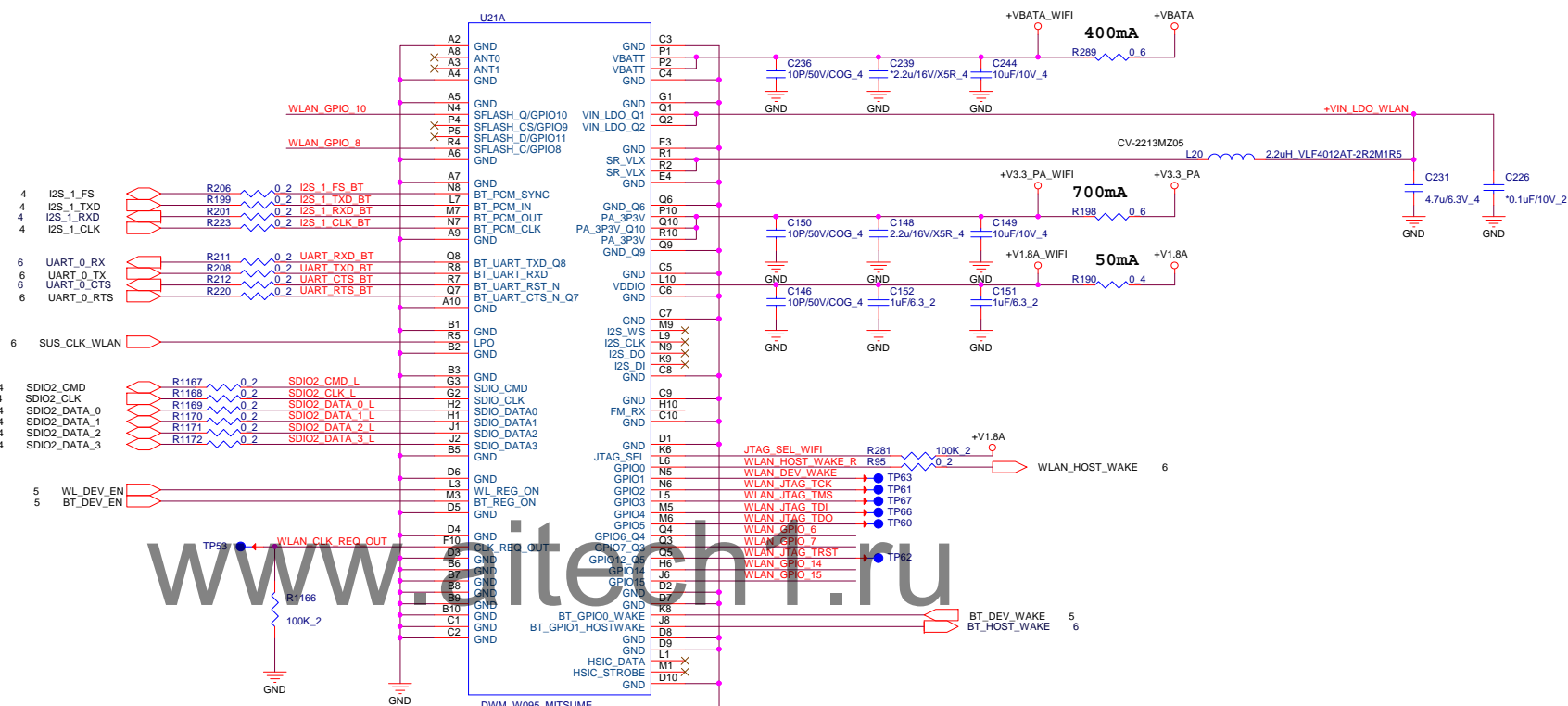


CAM\_1\_PWRDWN\_L High --> power down



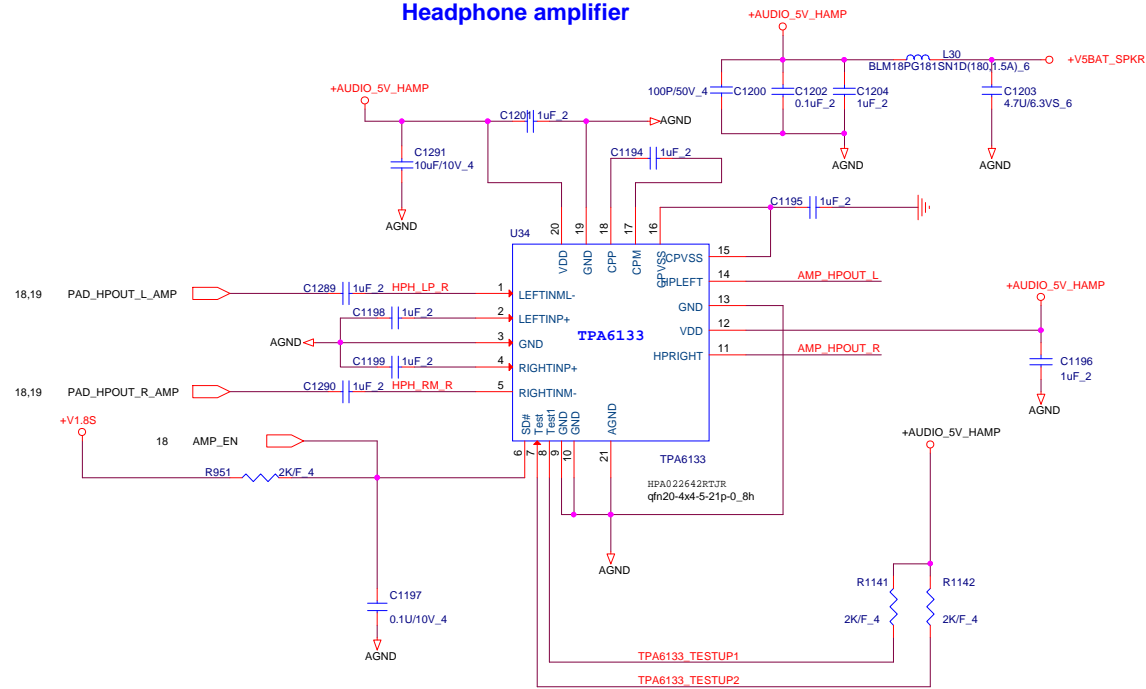


## WIFI&BT



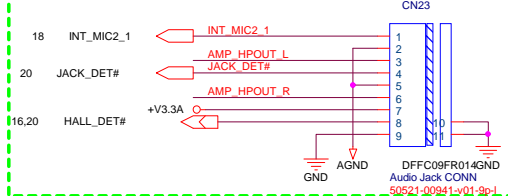


## Headphone amplifier

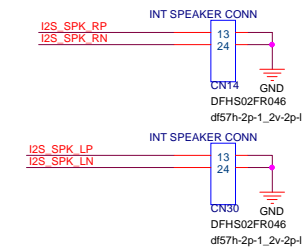


18,19 PAD\_HPOUT\_L\_AMP R1205 0.4 AMP\_HPOUT\_L  
18,19 PAD\_HPOUT\_R\_AMP R1206 0.4 AMP\_HPOUT\_R

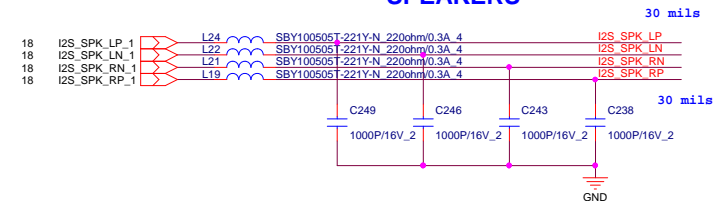
## Audio Jack SB



## Speak conn.

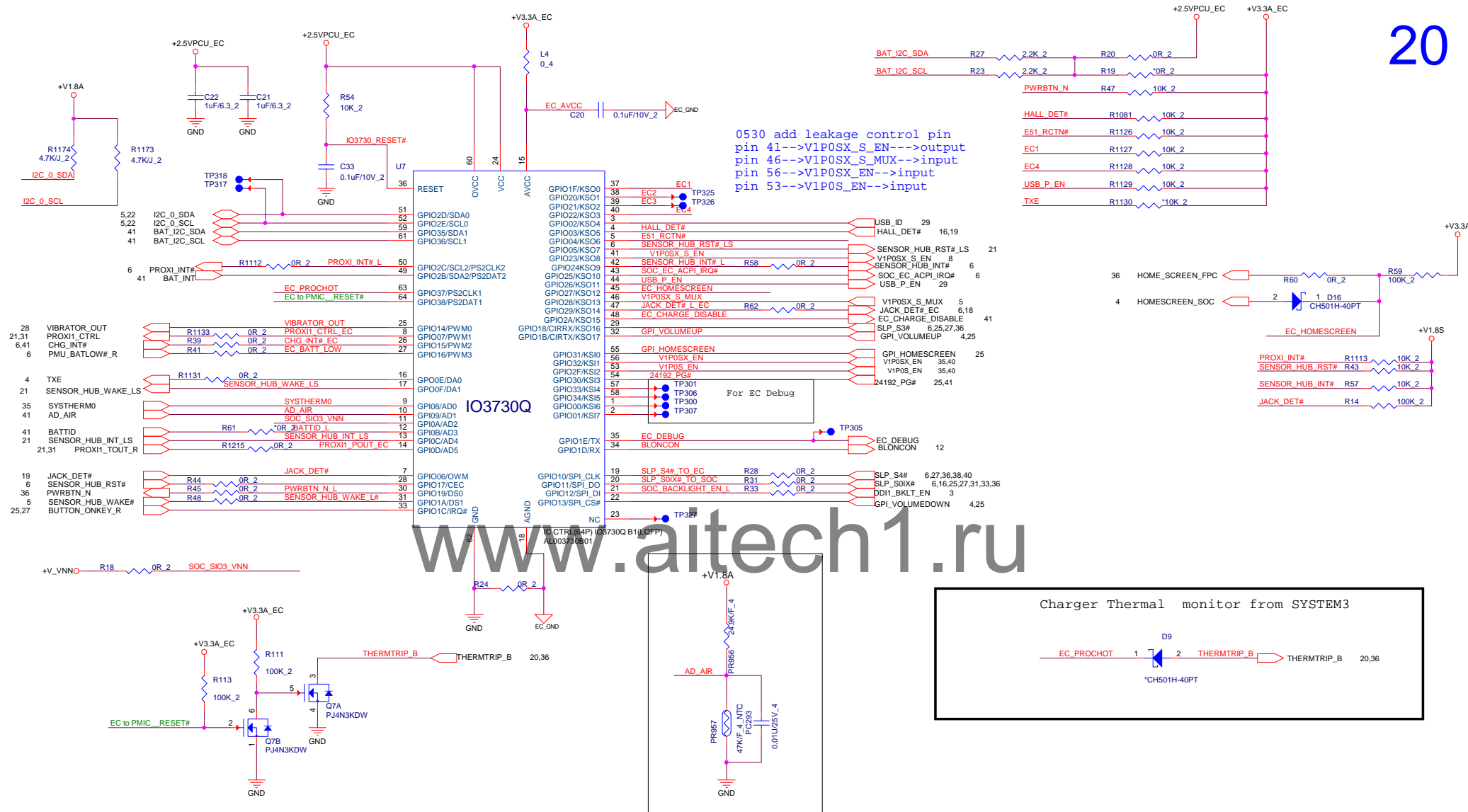


## SPEAKERS



**PROJECT : Cedar**  
**Quanta Computer Inc.**

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```
Backlight Control
HALL_INT / PMIC_BACKLIGHT_EN = 1
BLONCON = 1

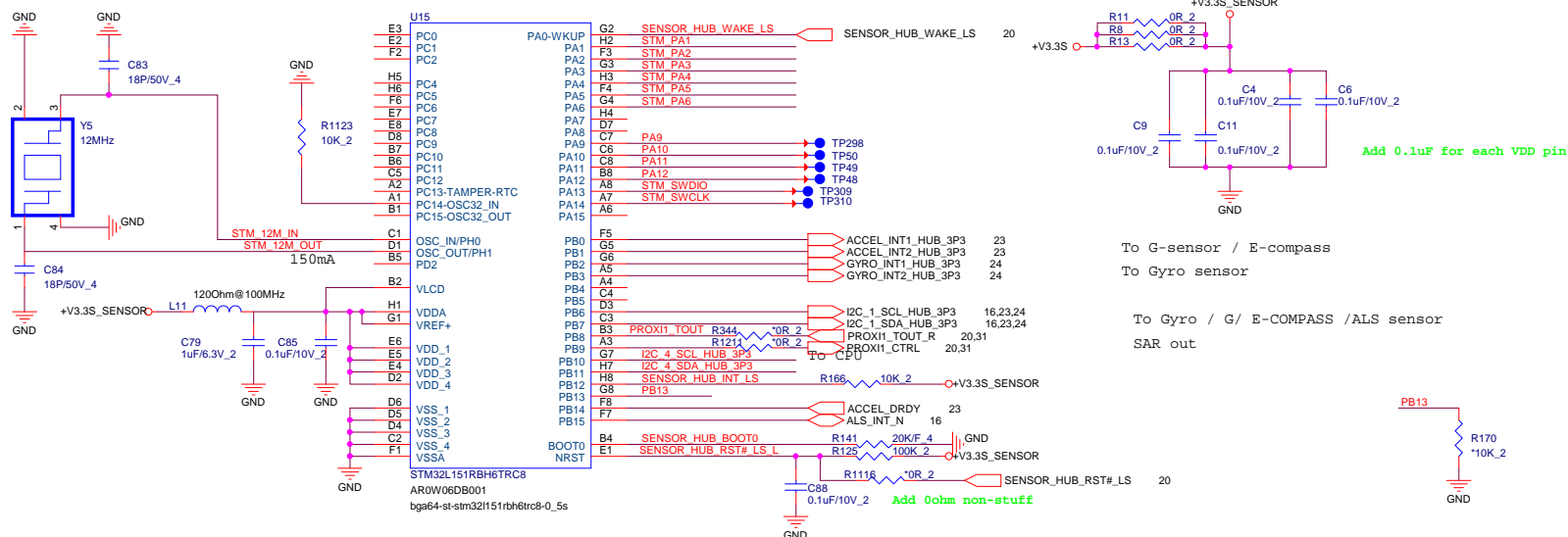
HALL_INT or PMIC_BACKLIGHT_EN = 0
BLONCON = 0
```

```
CPU Status (S0 or S5) = SLP_S3#
ACPI IRQ = SOC_EC_KEY_IN
Keyboard IRQ = SOC_EC_KB_IN
```

```
0529 power add thermistor
```

## Sensor HUB

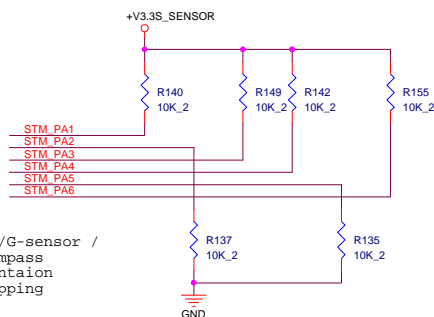
0528 change PN AR0W06DB001



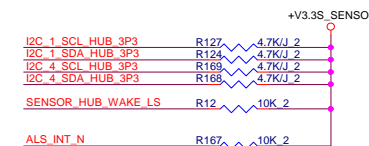
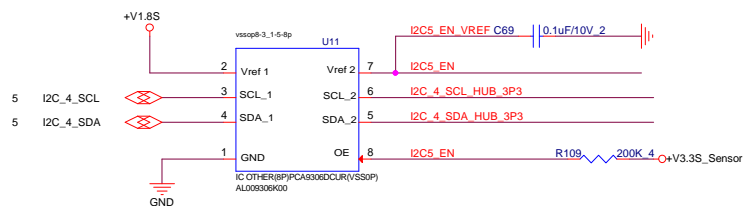
```

please ST strap setting guideline
PA1 ~ PA3 --> G + E-compass
PA4 ~ PA6--> Gyro
STM_PA1 : ?
STM_PA2 : ?
STM_PA3 : ?
STM_PA4 : ?
STM_PA5 : ?
STM_PA6 : ?

```

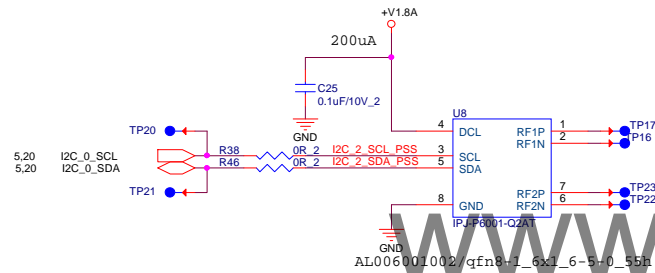


## Sensor level shifters

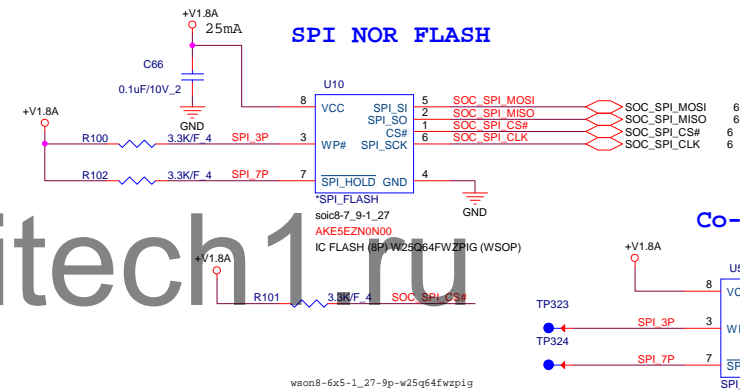


## PSS

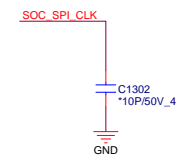
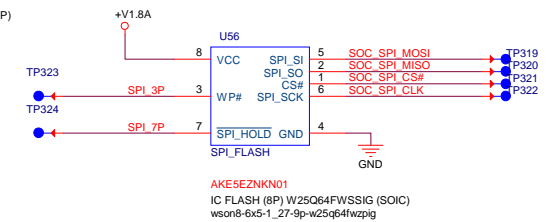
PSS (Platform Secure Storage)  
I2C 7-bit slave address (110111M)



## SPI NOR FLASH

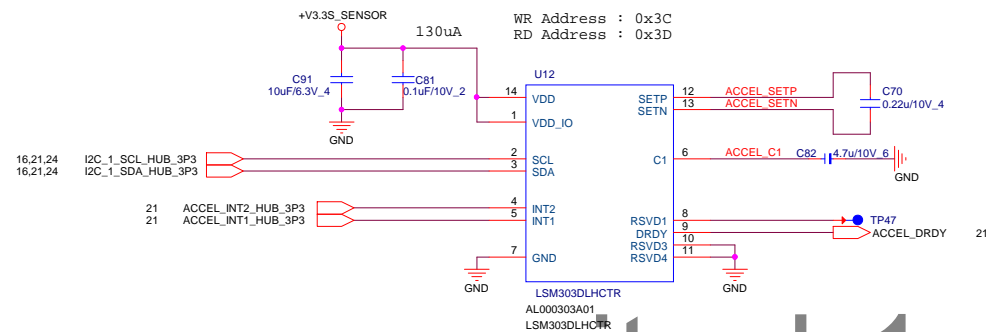


## Co-layout



0516 change G-sensor PN to AL000303A01

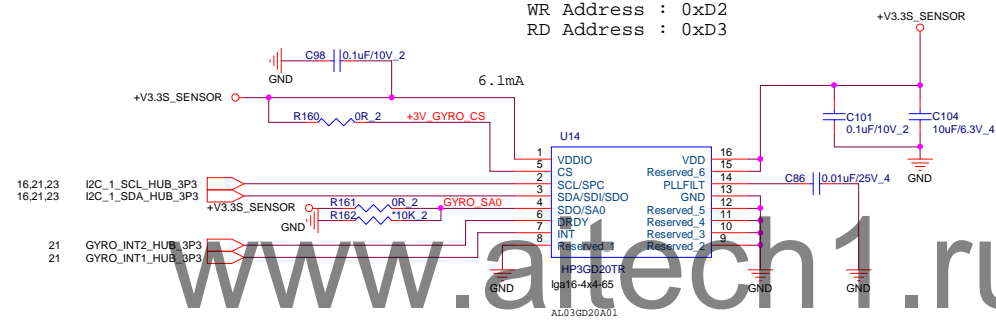
## G-sensor / E-compass



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## Gyroscope

WR Address : 0xD2  
RD Address : 0xD3





for Atmel un-stuff  
for N-Trig stuff

0527 delete N-trig solution

0603 modifyf

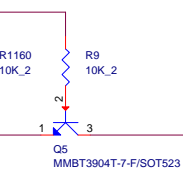
5 I2C\_5\_SDA  
5 I2C\_5\_SCL

I2C\_5\_SDA R1152 0R 2 I2C\_5\_SDA con  
I2C\_5\_SCL R1154 0R 2 I2C\_5\_SCL con

0603 modifyf

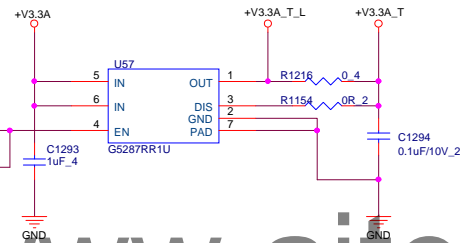
+2.5VPCU\_EC

+V1.8S

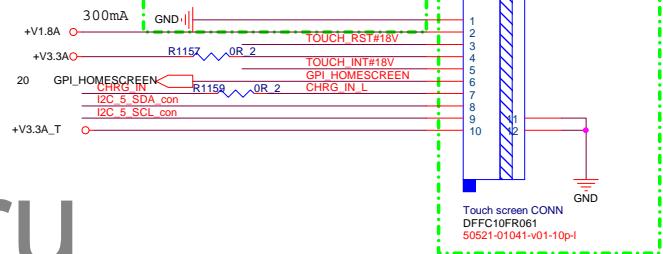


0603 modifyf

6 TOUCH\_INT# TOUCH\_INT# R1135 0R 2 TOUCH\_INT#18V

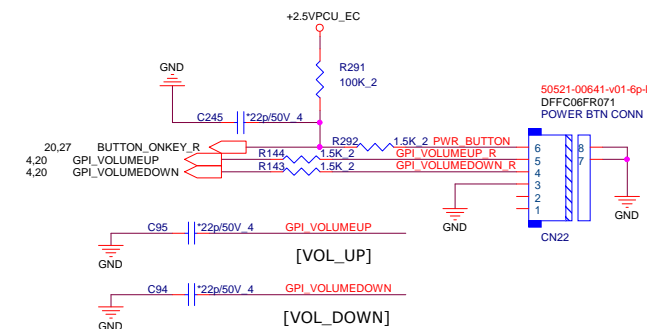


0603 modifyf  
remove R1158.

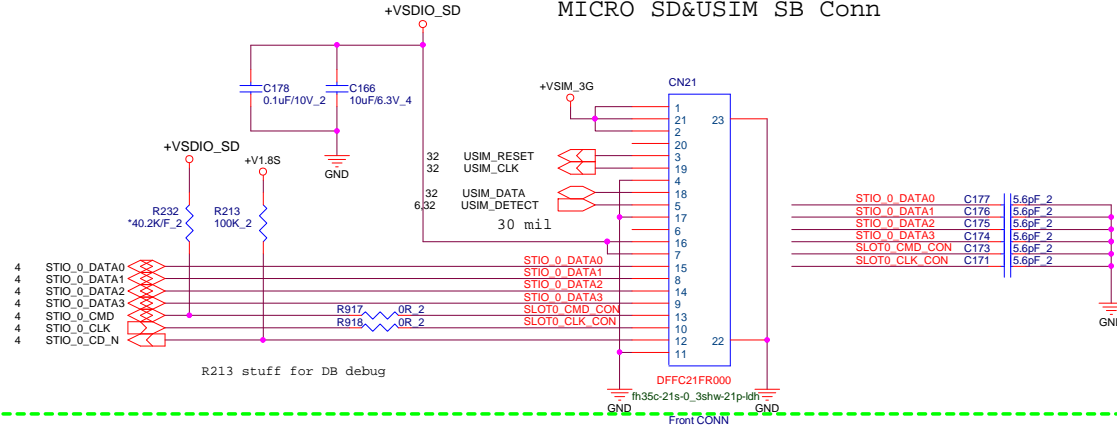


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## PWR & Vol Conn

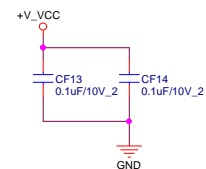
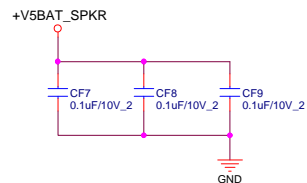
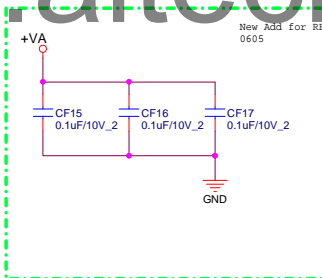
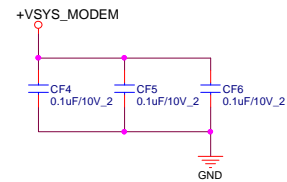
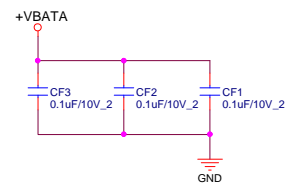
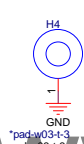
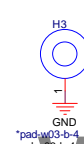
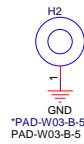
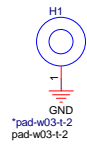
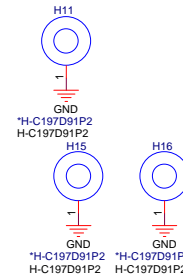
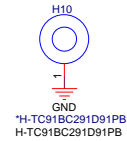
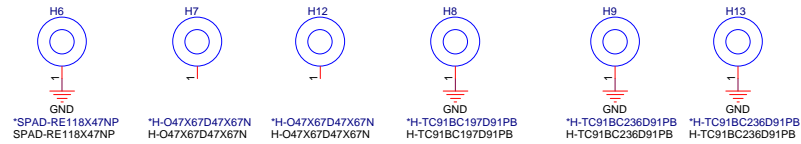


## MICRO SD&USIM SB Conn

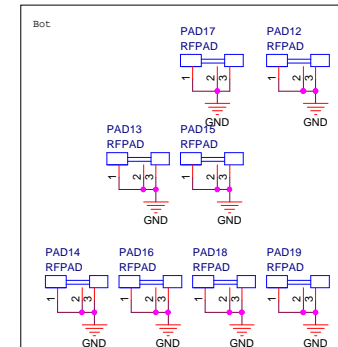
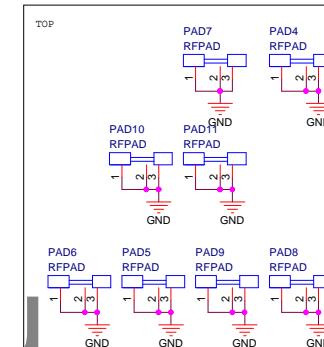


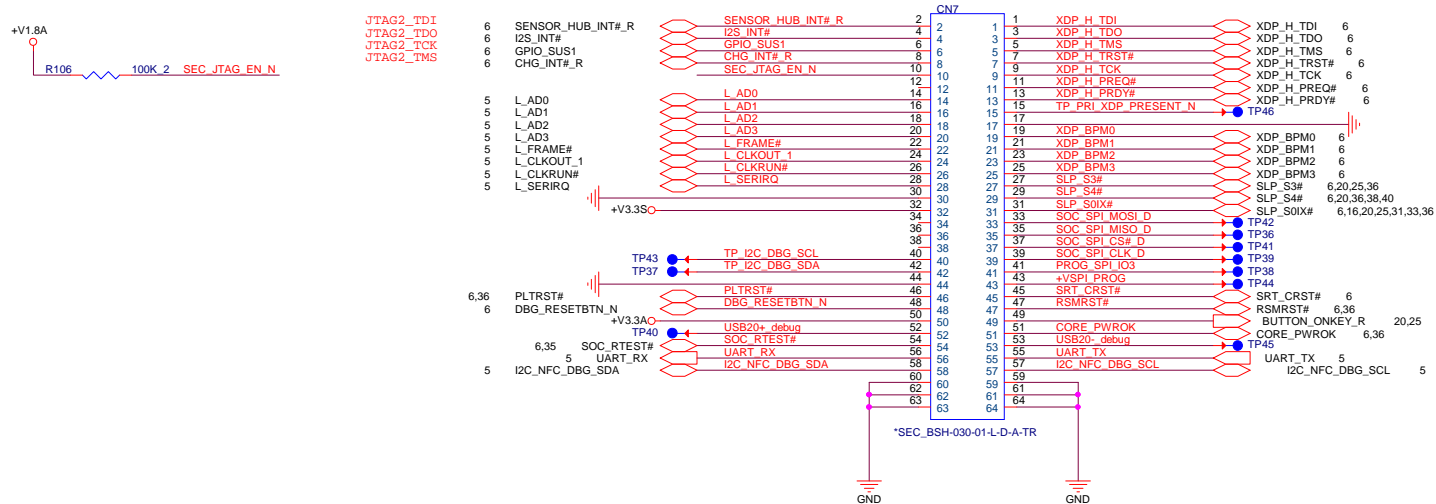
**PROJECT : Cedar**  
**Quanta Computer Inc.**

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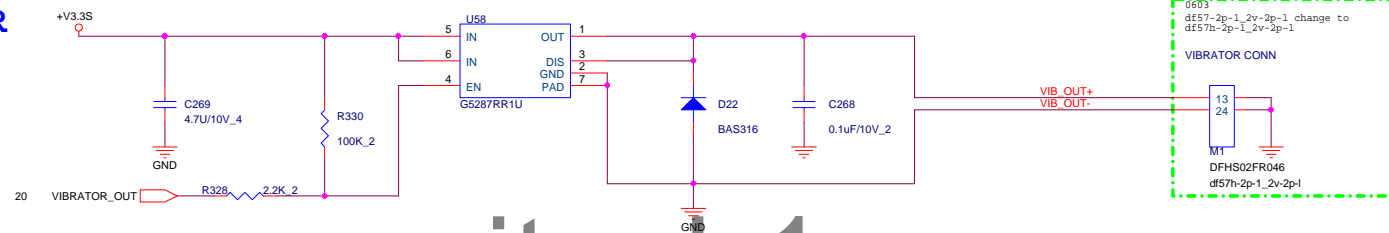
RF





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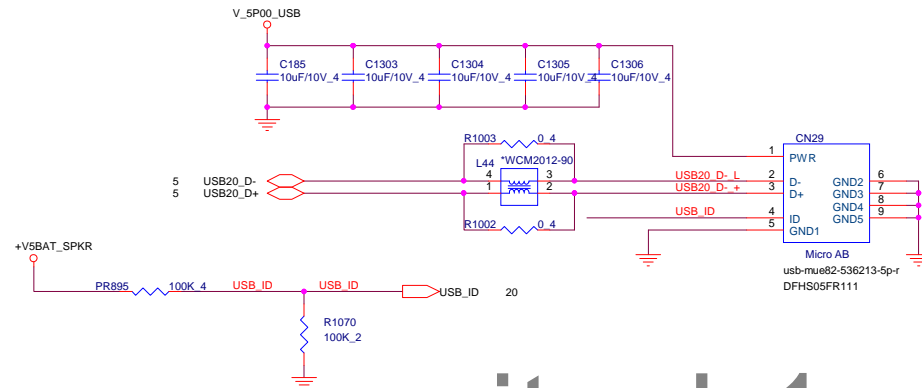
# VIBRATOR



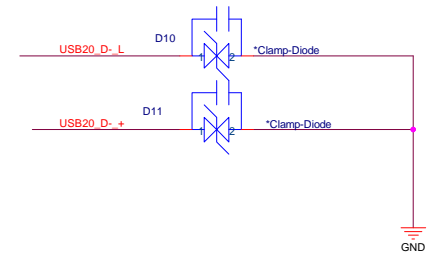
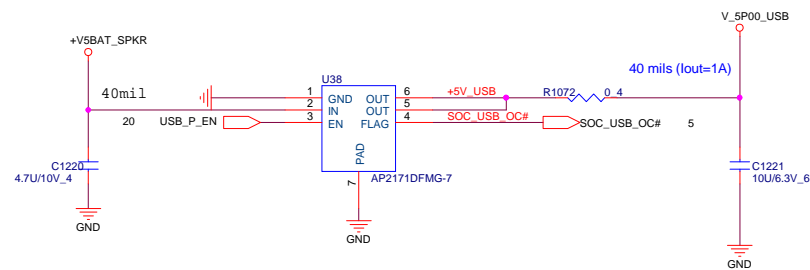
VIBRATOR low active

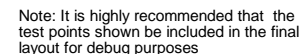
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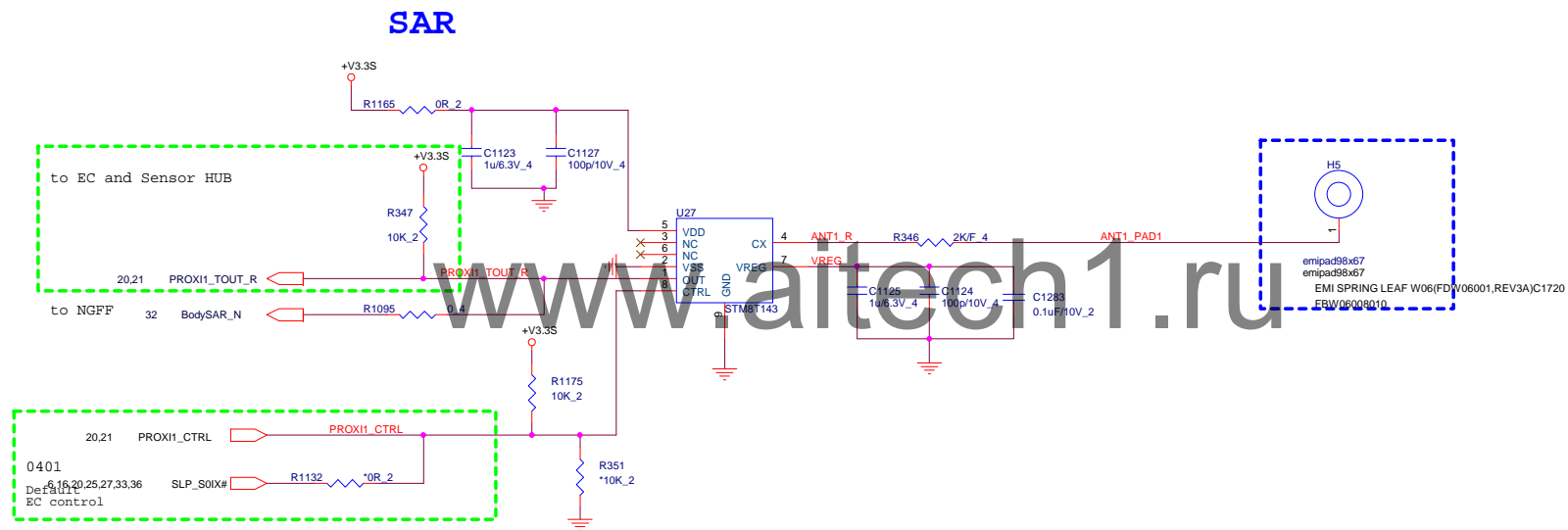
## uUSB Port



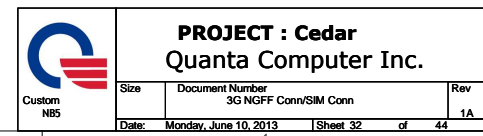
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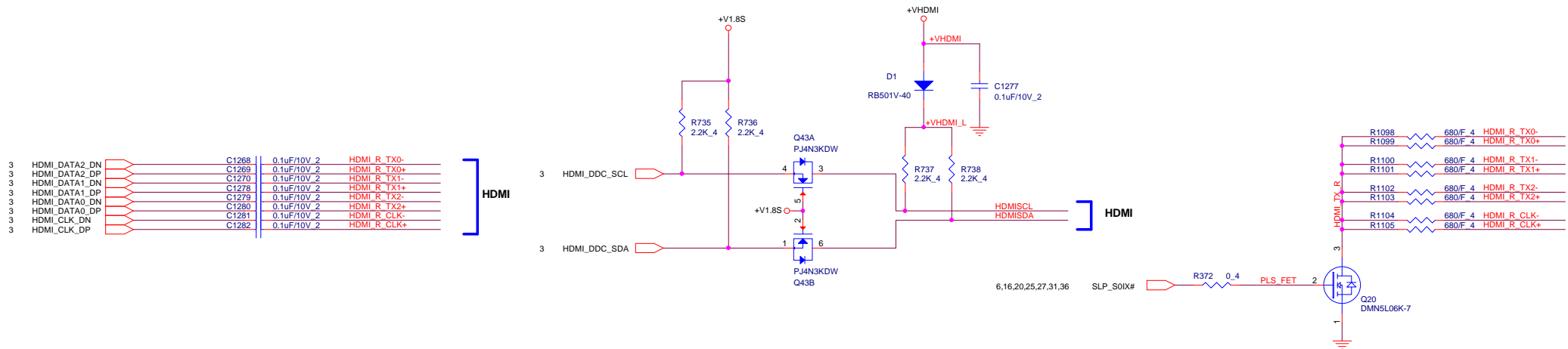




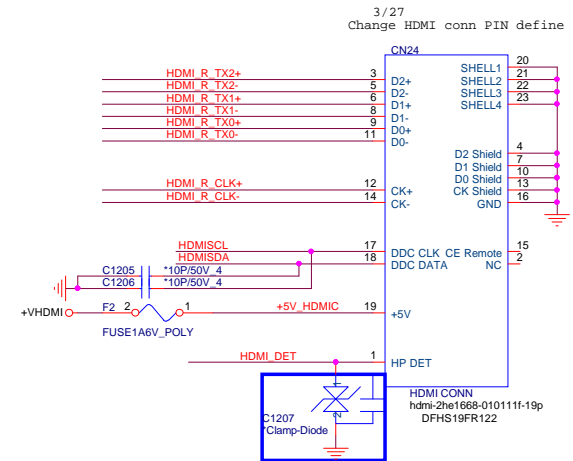
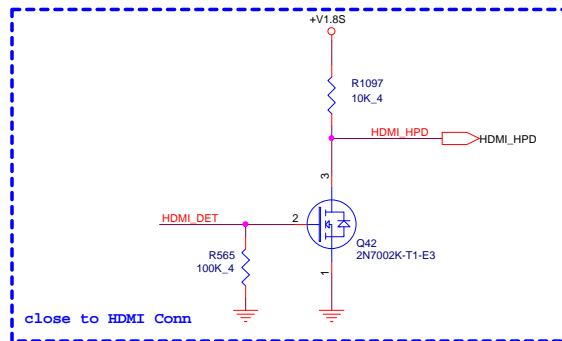
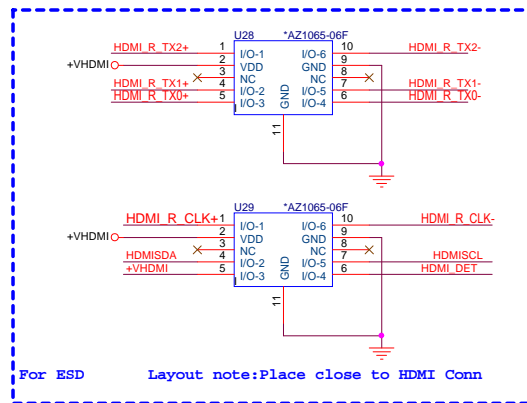
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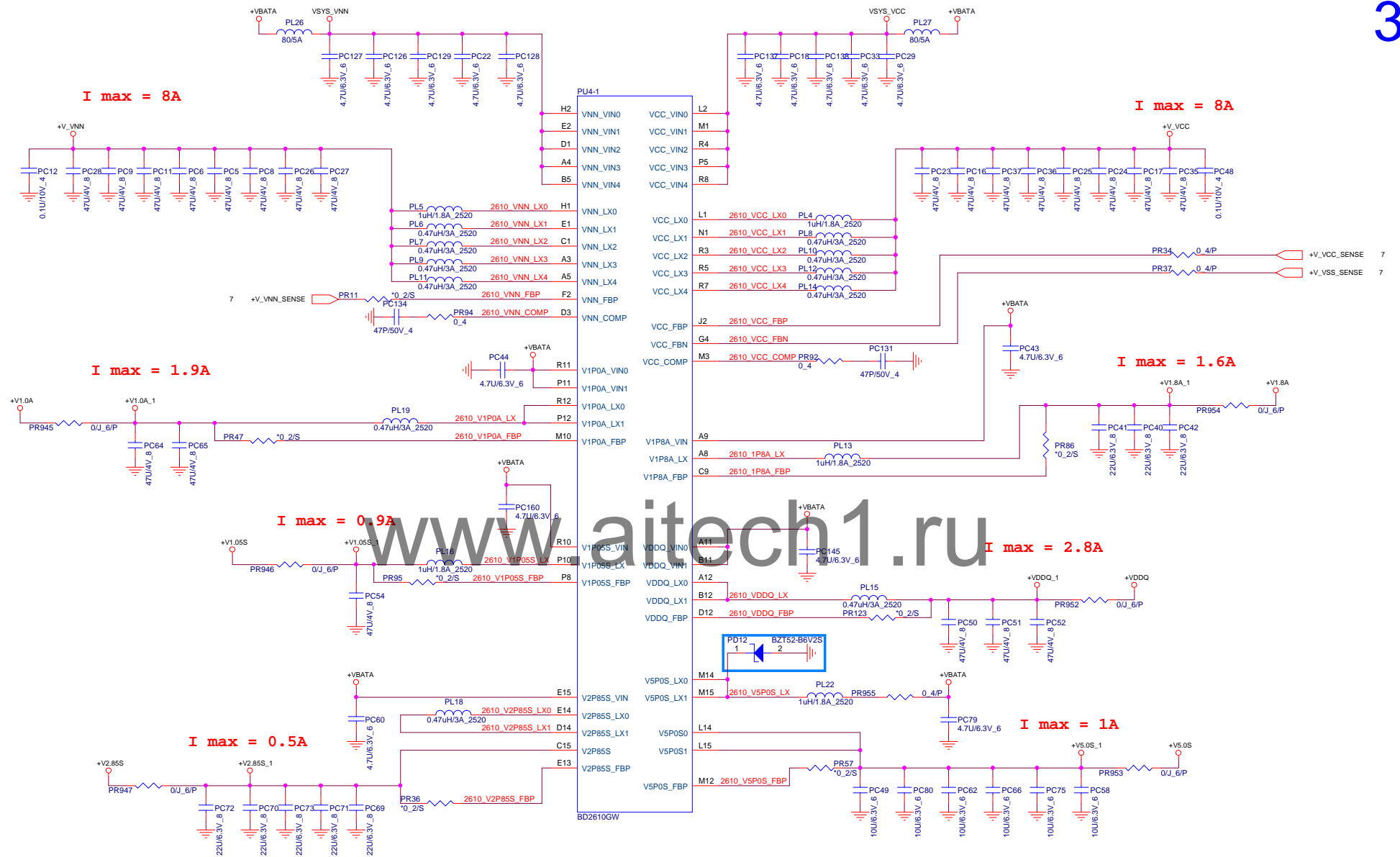


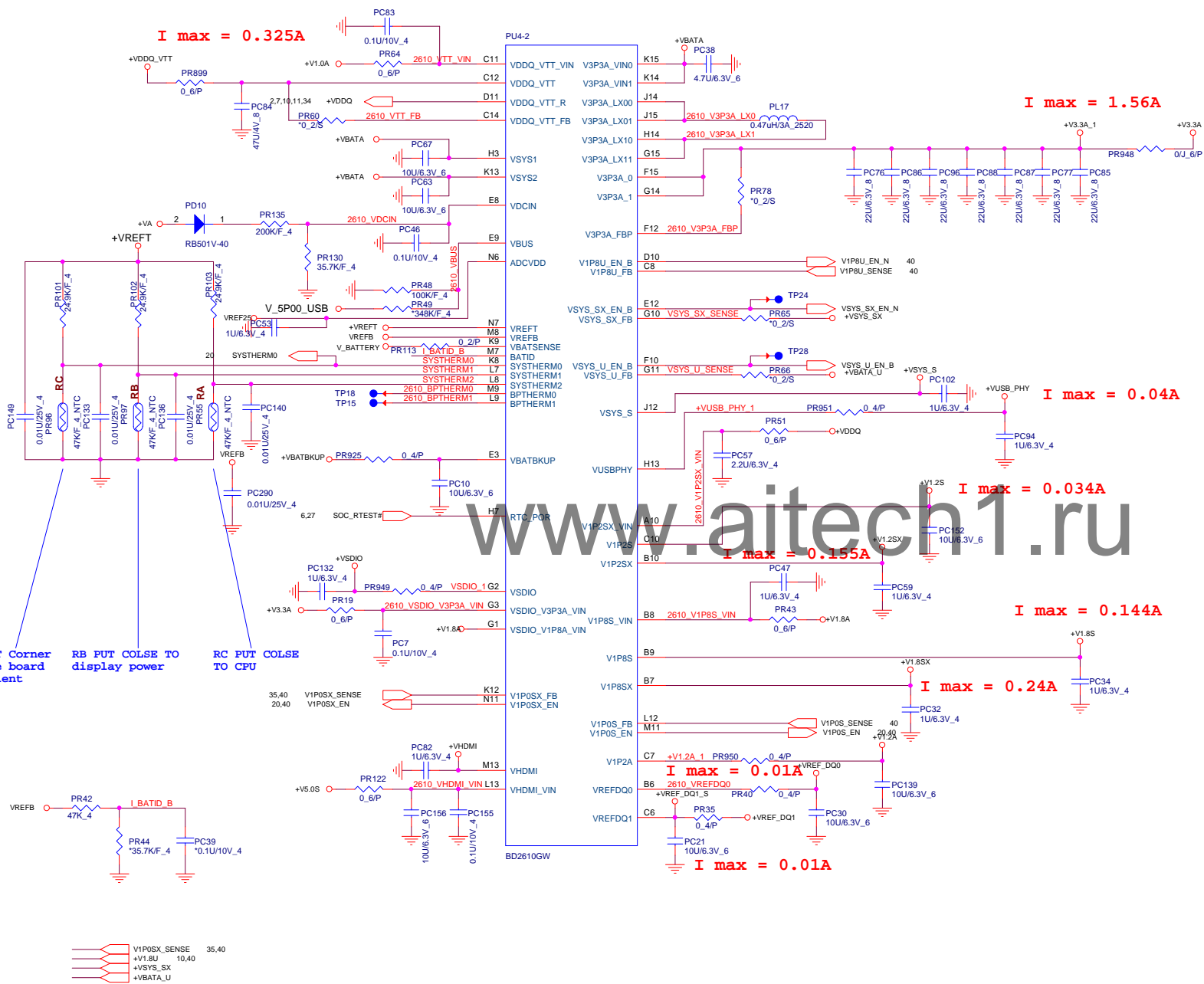


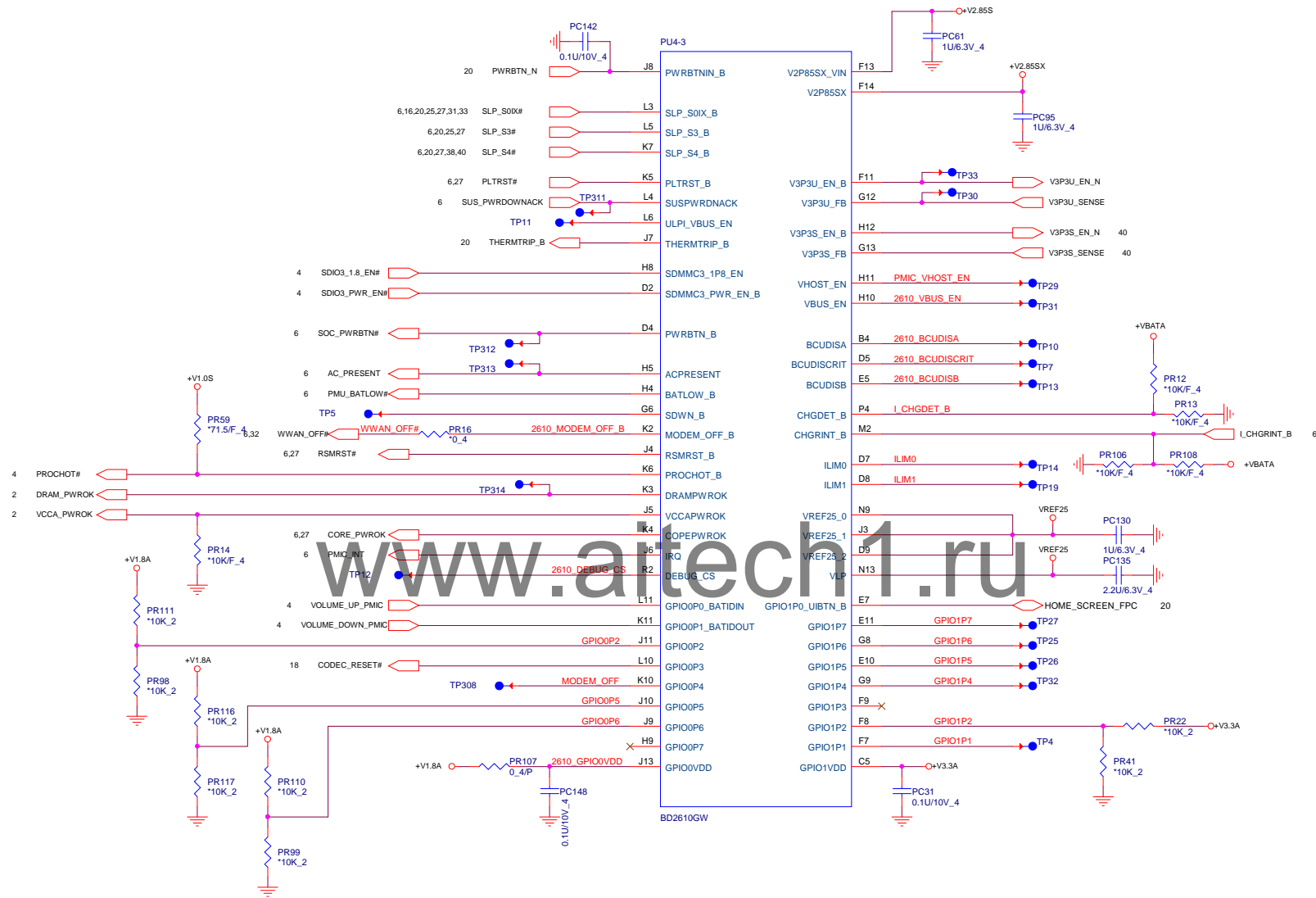


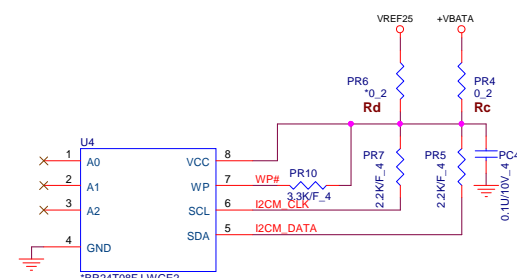
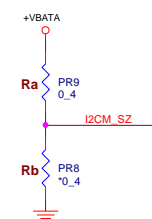
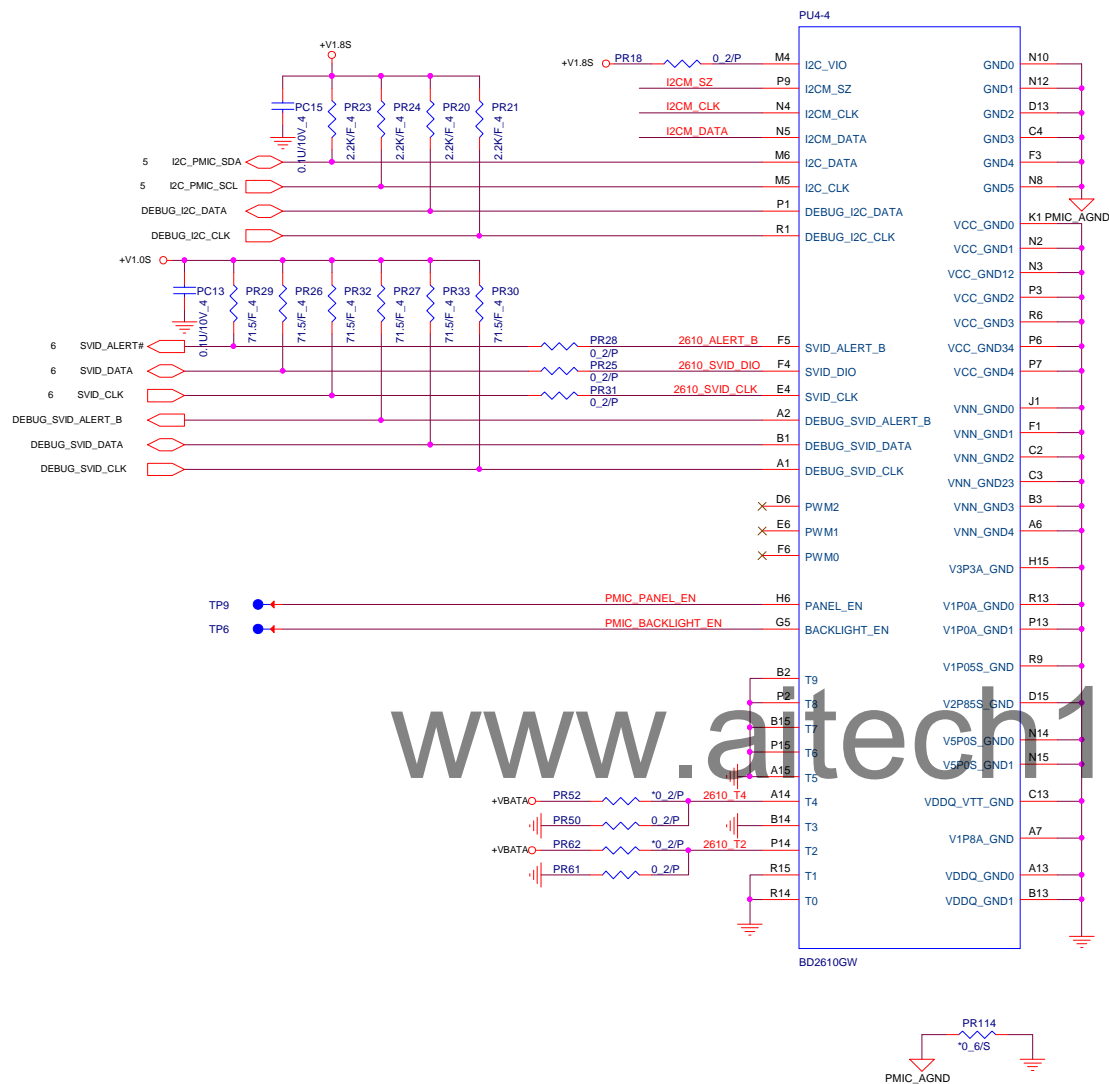
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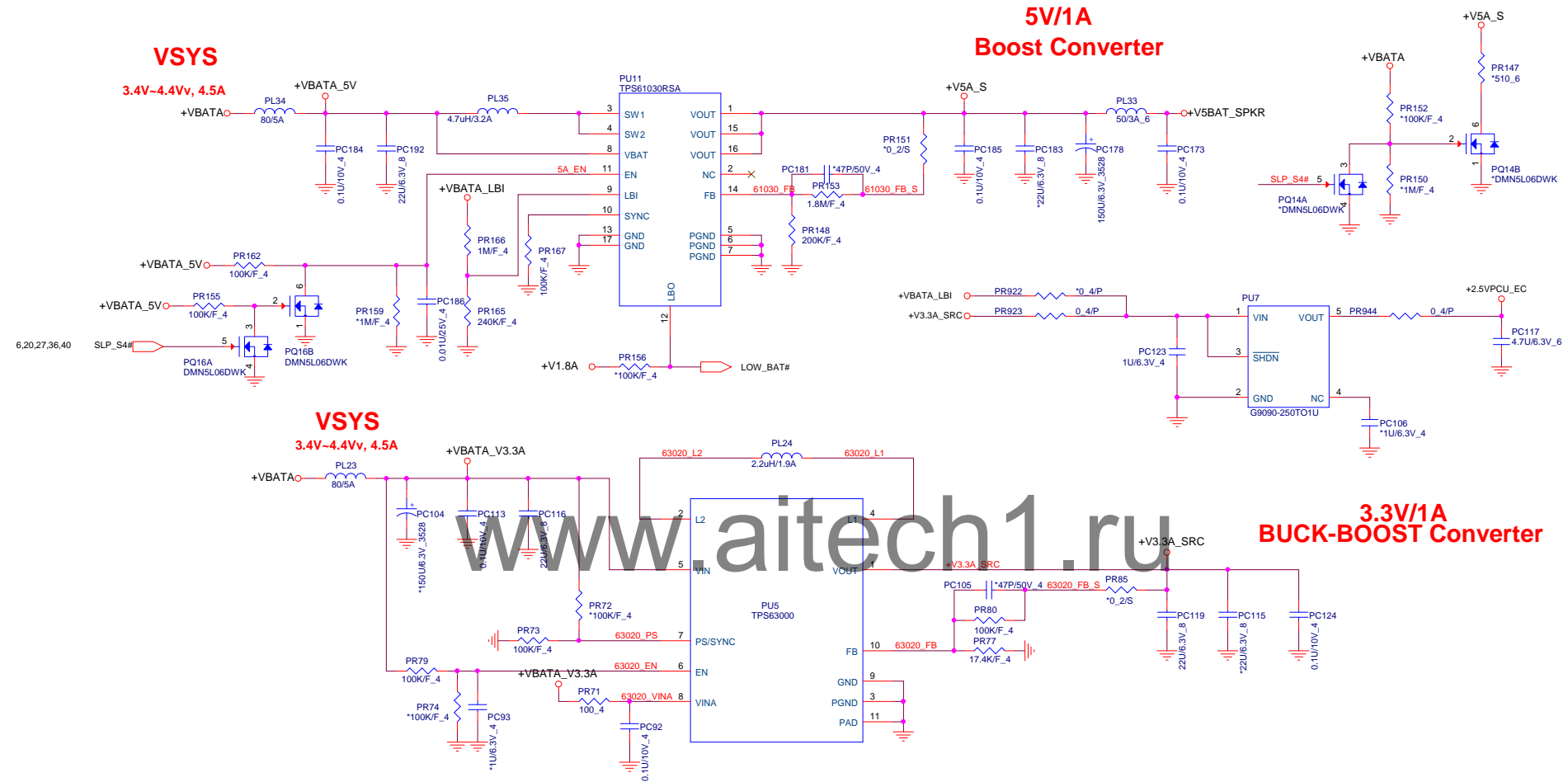


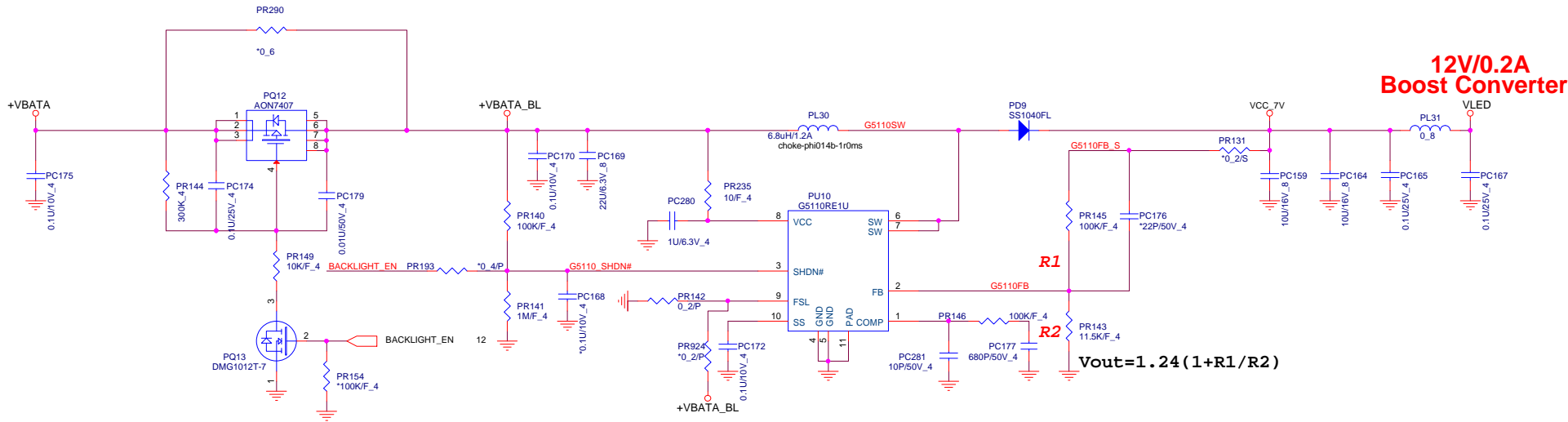


	A1	A2
Ra	No ASM	0
Rb	0	No ASM
Rc	No ASM	0
Rd	0	No ASM

A2 Support only 32bit or more size of eeprom


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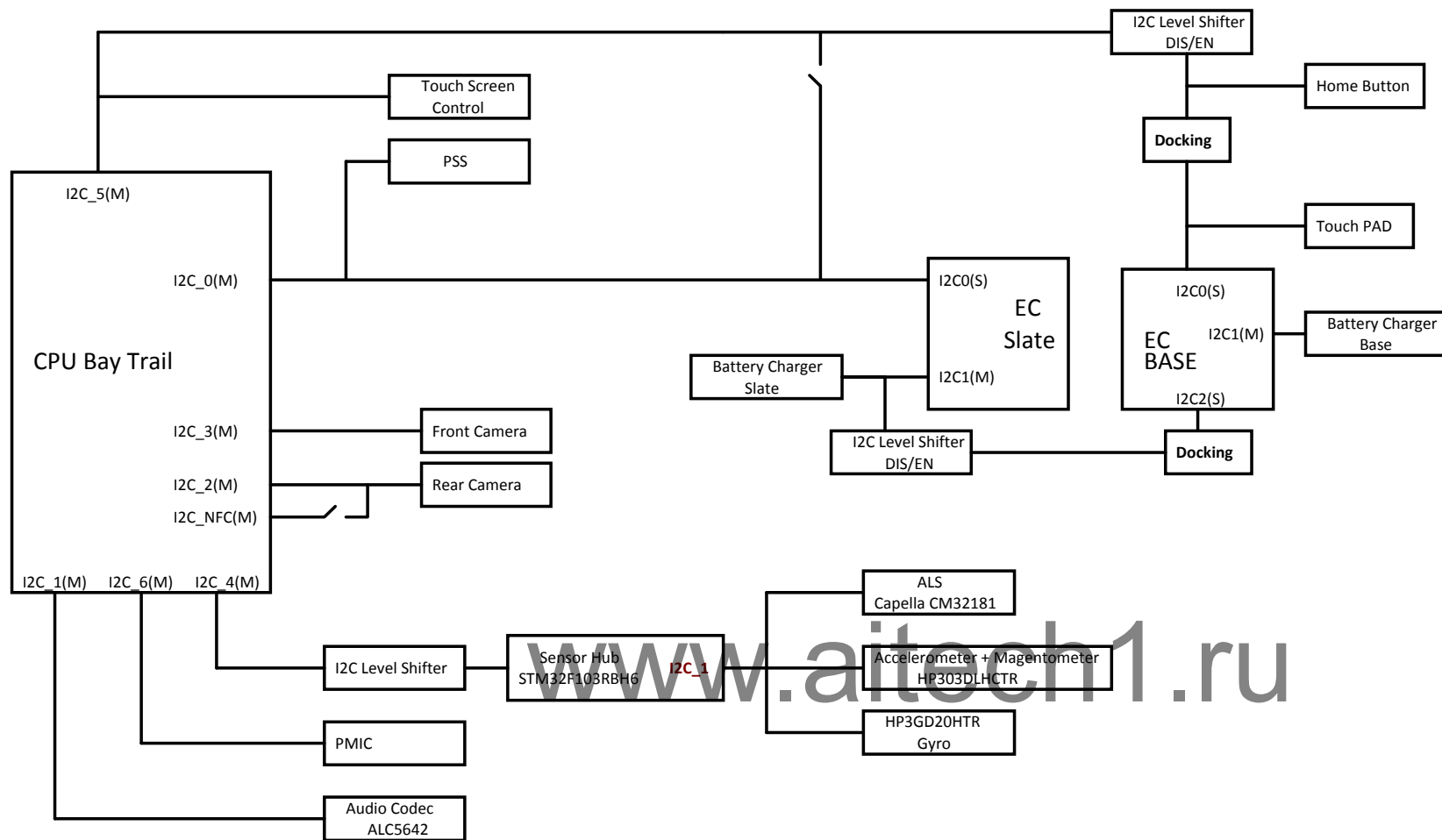
www.aitech1.ru



 NBS	<b>PROJECT : W03</b> <b>Quanta Computer Inc.</b>		
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# Baytrail FFRD Platform Power Sequencing : Cold Boot (Power Up)

